

Product Specification

**3G SDI Video Optical SFP Receiver Module
Dual Channels
with PIN Photodiodes**

EP-2RP-3G-H1

ePHOTON

Preliminary

EP-2RP-3G-H1

*3G SDI Video Optical Dual Receiver Module
With PIN Photodiode for SMPTE 292M/424M*

1 Features

- 1.1 Receiver unit with independent PINTIA
- 1.2 SMPTE 297-2006 compatible
- 1.3 Better than -20dBm sensitivity
- 1.4 Supports video pathological patterns for SD-SDI, HD-SDI and 3G-SDI
- 1.5 Diagnose and control via I²C interface including:
Monitoring of receiver optical power, supply voltage
and module temperature; Alarm reporting; Module ID polling
- 1.7 Industry standard SFP form factor;
- 1.8 Single +3.3V power supply
- 1.9 Operating temperature range: 0°C to 70°C (case)
- 1.10 Hot pluggable
- 1.11 RoHS-6 compliant

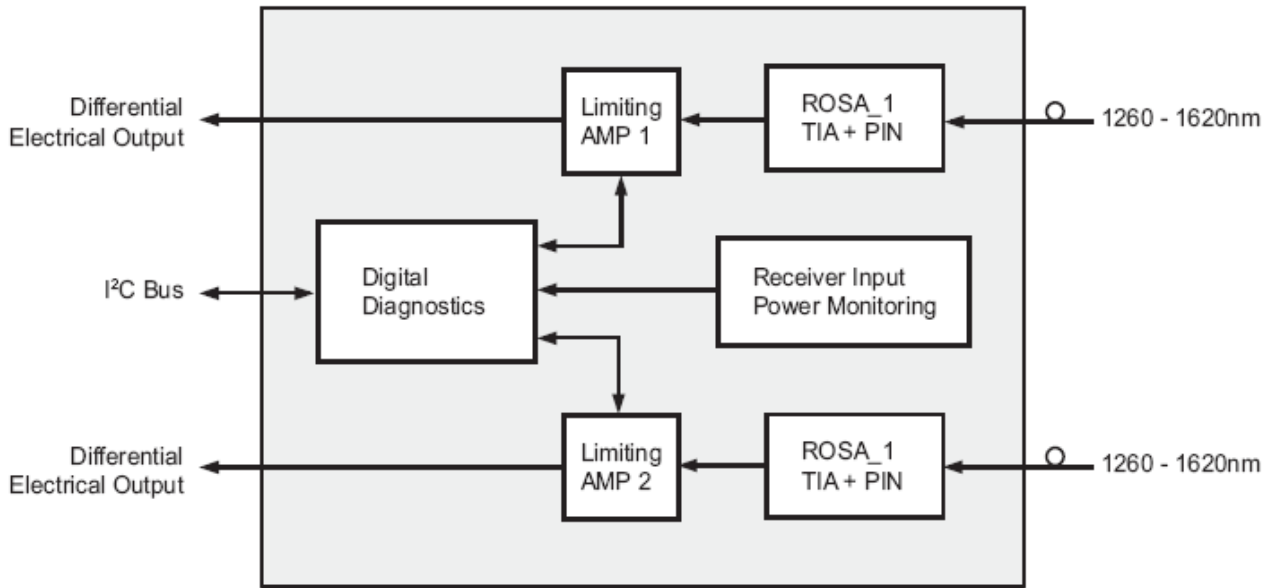
2 Applications

- 2.1 3G video application
- 2.2 SMPTE 297-2006 compatible OE interfaces
- 2.3 Broadcast cameras

3 General

The Modules are optical transceiver or transmitter and receiver modules designed to transmit and receive optical and electrical serial digital signals as defined in SMPTE 297-2006. The transceiver or transmitter and receiver are specifically designed for the application with the performance of SDI pathological patterns for SMPTE 259M, 344M, 292M and 424M serial rates.

4 Functional Block Diagram



5 Performance Specifications

5.1 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Note
Storage Temperature (Non-Operating)	T_{stg}	-40	+85	°C	
Case Temperature (Operating)	T_c	0	+70	°C	1
Power Supply Voltage	$V_{cc}-V_{ee}$	-0.5	+3.6	V	
Lead Soldering	T/Sec		260/10	°C/Sec	
Receiver Optical Input Power			5	dBm	
Relative Humidity(non-condensing)		5%	95%	RH	

Note

1. Measured on top side front center of SFP module.

5.2 Operating Environment

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage	V_{cc}	+3.1	+3.3	+3.5	V	-
Power Consumption	I_{cc}	-	600	800	mW	-
Case Temperature (Operating)	T_c	0	-	70	°C	-
Data Rate	-	-	-	2.97	Gb/s	-

5.3 Receiver Specifications

(Over Operating Case Temperature Range, Vcc = 3.135V to 3.465V)

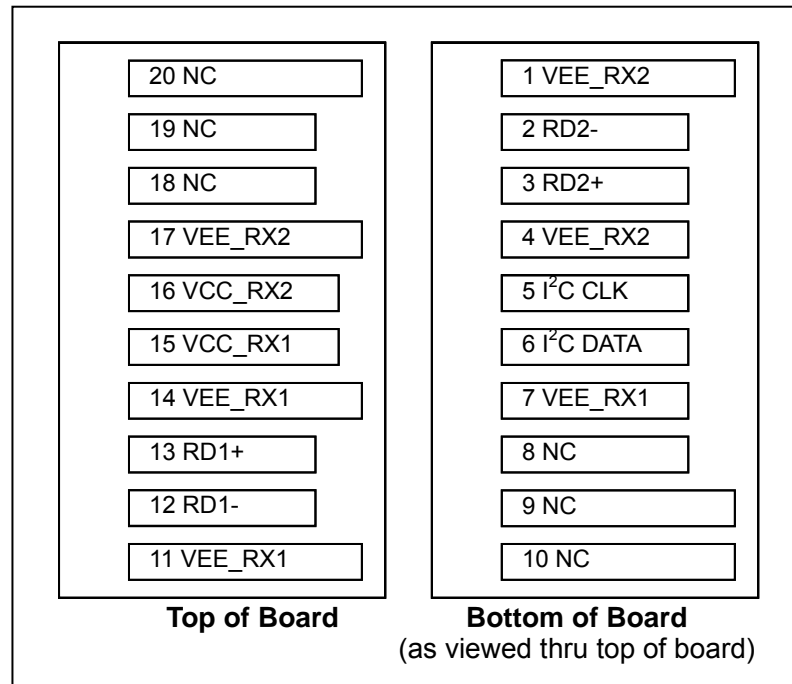
Electrical Characteristics						
Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Differential Data Output Swing	$V_{out,pp}$	600	-	1000	mV	-
Output Resistance	R_{in}	42	50	58	ohm	-
Optical Characteristics						
Optical Sensitivity	P_{sen}	-	-22	-20	dBm	1,2
Optical Overload	P_{sat}	0	-	-	dBm	1,2
Optical Wavelength(Channel 1)	λ_1	1260	-	1620	nm	3
Optical Wavelength(Channel 2)	λ_2	1260	-	1620	nm	3
Loss of Signal Asserted		-31	-	-	dBm	
Loss of Signal De-Asserted		-	-	-23	dBm	
Loss of Signal Optical Hysteresis		0.5	-	-	dB	
Maximum Back Reflection		-	-	-27	dB	-

Notes:

1. Using 9/125 SMF
2. BER=1E-12 against both PRBS and pathological patterns at SMPTE 259M, 292M, 424M rates
3. Channel 1 and 2 definition, see the information in "8 Package Information"

6 Pin Definitions

6.1 Pin Diagram



6.2 Pin Descriptions

Pin#	Name	Function	Notes
1	VEE_RX2	Receiver Ch2 Ground	-
2	RD2-	Negative Differential Output of Ch2	AC Coupled Internally
3	RD2+	Positive Differential Output of Ch2	AC Coupled Internally
4	VEE_RX2	Receiver Ch2 Ground	-
5	I ² C CLK	I ² C Clock	Note 1, 2 wire serial ID interface
6	I ² C DATA	I ² C, Data	Note 1, 2 wire serial ID interface
7	VEE_RX1	Receiver Ch1 Ground	-
8	NC	Not Connect	
9	NC	Not Connect	
10	NC	Not Connect	
11	VEE_RX1	Receiver Ch1 Ground	-
12	RD1-	Negative Differential Output of Ch1	AC Coupled Internally
13	RD1+	Positive Differential Output of Ch1	AC Coupled Internally
14	VEE_RX1	Receiver Ch1 Ground	-
15	VCC_RX1	Receiver 1 Power Supply	Note 2, 3.3V 5%
16	VCC_RX2	Receiver 2 Power Supply	Note 2, 3.3V 5%
17	VEE_RX2	Receiver Ch2 Ground	-
18	NC	Not Connect	
19	NC	Not Connect	
20	NC	Not Connect	

Notes:

1. These are the module definition pins. They should be pulled up with a 4.7K–10K Ω resistor on the host board. The pull-up voltage shall be VCC.
2. VCC_RX1 and VCC_RX2 are the receiver 1 and 2 power supplies. They are defined as 3.3V \pm 5% at the SFP connector pin. Maximum supply current is 1100mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 Ω should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value.
Each channel has an independent power connection.

6.3 Optical Connector

An LC connector with PC/UPC is required for each port.

7. Serial interface memory map

Module identification and diagnostic information is accessible through the address map shown in the following tables. Block addresses starting with the letter "A" refer to Channel 1 and block addresses starting with the letter "B" refer to Channel 2

7.1 EEPROM serial ID memory contents (A0h)

Addr	Hex	ASCII	Description	Addr	Hex	ASCII	Description	Addr	Hex	ASCII	Description
0	82		Dual Receiver	32	20		Vendor name	64	00		
1	04		SFP function is defined by serial ID only	33	20			65	03		
2	07		LC connector	34	20			66	00		BR, Max
3	41		SMPTE259M/344M/292M/424M and 297M	35	20			67	00		BR, Min.
4	00		Reserved(SFP MSA does not specify SMPTE compliance codes)	36	XX		Reserved	68	XX		Vendor Serial number
5	00			37	00		Vendor OUI	69	XX		
6	00			38	00			70	XX		
7	00			39	00			71	XX		
8	00			40	45	E	Vendor part number	72	XX		
9	00			41	50	P		73	XX		
10	00			42	2D	-		74	XX		
11	03			NRZ	43	32		2	75	XX	
12	1E			BR in 100Mbps	44	52	R	76	XX		
13	00			Reserved	45	50	P	77	XX		
14	00		Length(9u)*Km	46	2D	-	78	XX			
15	00		Length(9u)*100m	47	33	3	79	XX			
16	00		Length(50u)*10m	48	47	G	80	XX			
17	00		Length(62.5u)*10m	49	2D	-	81	XX			
18	00		Length(Copper)	50	48	H	82	XX			
19	XX		Reserved	51	31	1	83	XX			
20	65	E	Vendor name	52	20			84	XX		Vendor data code: Year
21	50	P		53	20			85	XX		
22	48	H		54	20			86	XX		Vendor data code: Month
23	4F	O		55	20			87	XX		
24	54	T		56	00			88	XX		Vendor data code: Day
25	4F	O		57	00			89	XX		
26	4E	N		58	00			90	20		Blank
27	20			59	00		Vendor PN reversion preliminary	91	20		
28	20			60	20		NA	92	28		
29	20			61	20		NA	93	90		Enhanced alarm/warning flags
30	20		62	00		Reserved	94	XX		Reserved	
31	20		63	XX		CC_BASE(0-62)	95	XX		CC_EXT(64-94)	

96-127: 00, Vendor specific
 128-255: 00, Reserved
 XX: denotes hex values which varies from module to module

7.2 EEPROM serial ID memory contents (A2h and B2h)
7.2.1 Alarm and Warning Thresholds, OM Output Calibration ID Fields

Addr. (DEC)	# Bytes	Name	Value
00-01	2	Temp high alarm	+120°C
02-03	2	Temp low alarm	-13°C
04-05	2	Temp high warning	+110°C
06-07	2	Temp low warning	-8°C
08-09	2	Supply voltage high alarm	+3.6V
10-11	2	Supply voltage low alarm	+3.0V
12-13	2	Supply voltage high warning	+3.47V
14-15	2	Supply voltage low warning	+3.14V
16-31	16	NA	
32-33	2	Rx Power High Alarm	1.5dBm
34-35	2	Rx Power Low Alarm	-25dBm
36-37	2	Rx Power High Warning	0dBm
38-39	2	Rx Power Low Warning	-23dBm
40-94	59	Reserved	
95	1	Check sum	Byte 95 contains the low order 8 bits of the sum of bytes 0-94

7.2.2 A/D Values and Status Bits

Addr. (DEC)	Bit	Name	Description
96	All	Temperature MSB	Internally measured module temperature, see table 7.2.3
97	All	Temperature LSB	
98	All	Vcc MSB	Internal measured supply voltage, see table 7.2.3
99	All	Vcc LSB	
100-103	All	NA	
104	All	Rx Power MSB	Internally Measured Rx Power
105	All	Rx Power LSB	Internally Measured Rx Power
106-109	All	Reserved	
Optional status/control bits			
110	7-3		Reserved
110	2	N/A	
110	1	LOS	Indicates Rx_LOS state
110	0	Data_Ready	
111	7	Temp_Update	Goes to high after a temperature update
	6	Vcc_Update	Goes to high after a Vcc update
	5	Mon 1 Update	Goes to high after a Tx bias current update
	4	Mon 2 Update	Goes to high after a TX power update
	3	Mon 3 Update	Goes to high after a Rx input power update
	2-0	Reserved	

7.2.3 Receiver only

A2h address	Parameter	Calibration	Accuracy	Range
96-97	Temperature	Internal	+/- 3°C	Note 1
98-99	Vcc	Internal	+/- 3%	3.135 to 3.465
104-105	Rx power	External	+/- 3dB	-23 to 0dBm

Notes:

1. Junction temperature of temperature sensing device

7.2.4 Alarm and Warning Flags

Addr. (DEC)	Bit	Name	Description
112	7	Temp high alarm	Set when internal temperature exceeds high alarm level
	6	Temp low alarm	Set when internal temperature below low alarm level
	5	Vcc high alarm	Set when internal supply voltage exceeds high alarm level
	4	Vcc low alarm	Set when internal supply voltage below low alarm level
	3-0	NA	
113	7	Rx power high alarm	Set when the monitored Rx power exceeds the Rx power high alarm threshold
	6	Rx power low alarm	Set when the monitored Rx power goes below the Rx power low alarm threshold
	5-0	Reserved	
114-115	All	Reserved	
116	7	Temp high warning	Set when internal temperature exceeds high warning level
	6	Temp low warning	Set when internal temperature below low warning level
	5	Vcc high warning	Set when internal supply voltage exceeds high warning level
	4	Vcc low warning	Set when internal supply voltage below low warning level
	3-0	N/A	
117	7	Rx Power High Warning	Set when the monitored Rx power exceeds the Rx power high warning threshold
	6	Rx Power Low Warning	Set when the monitored Rx power goes below the Rx power low alarm threshold
	5-0	Reserved	
118-119	all	Reserved	

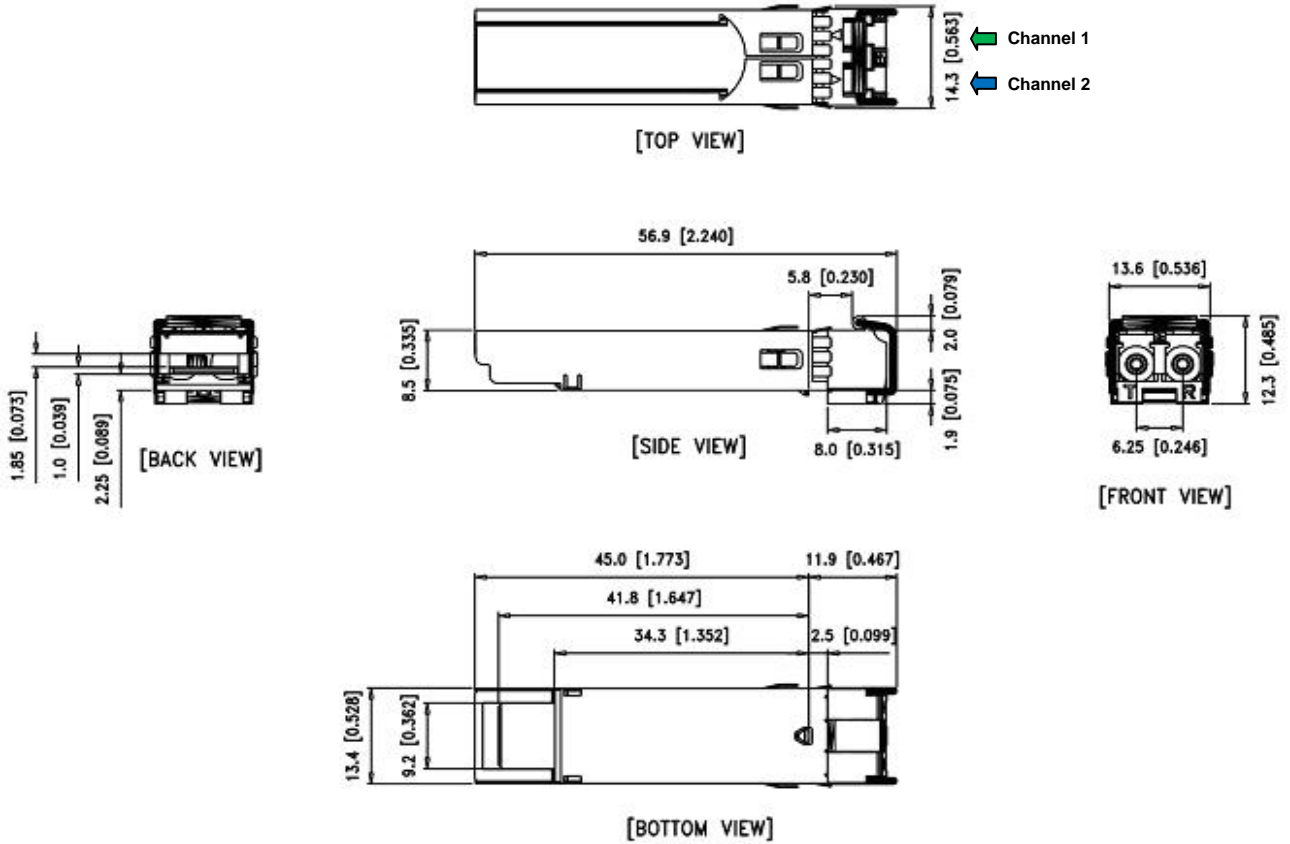
7.2.5 Vendor Specific Memory Addresses

Addr. (DEC)	# Bytes	Name	Description
120-127	8	Vendor specific	Vendor specific

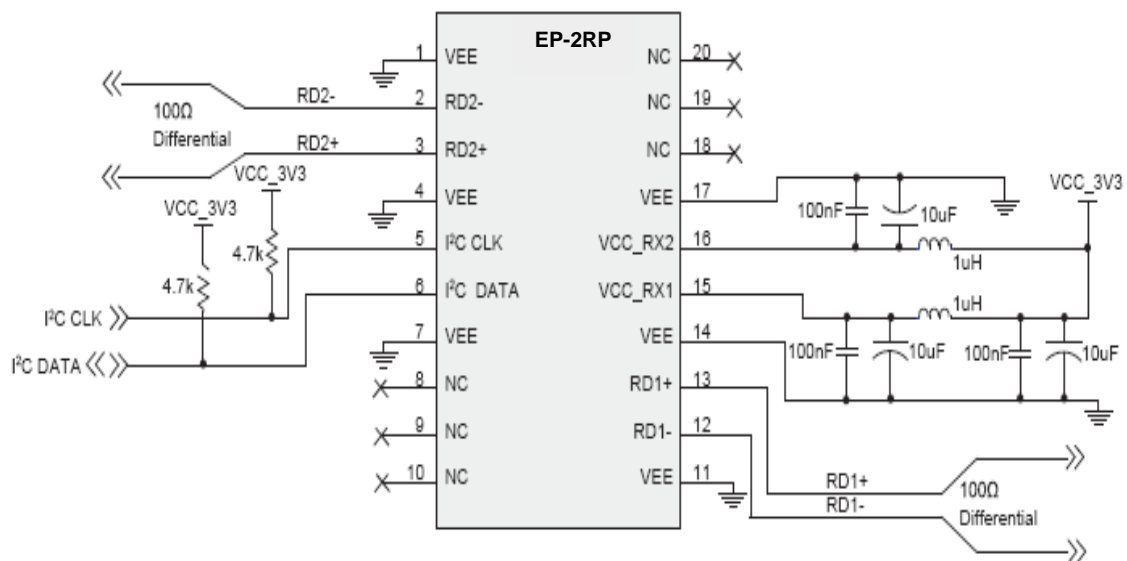
7.2.6 User EEPROM

Addr. (DEC)	# Bytes	Name	Description
128-247	120	Vendor Writable Area	Vendor writable EEPROM
248-255	8	Vendor specific	Vendor specific control functions

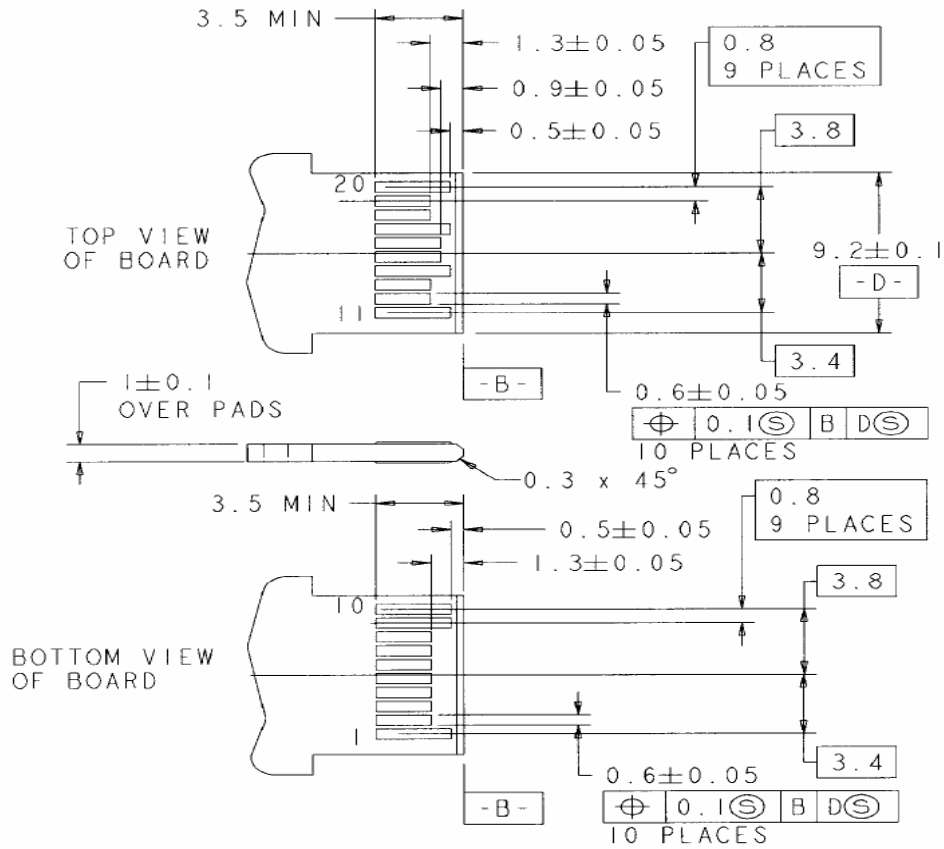
8 Package Information



9 Recommended Circuit

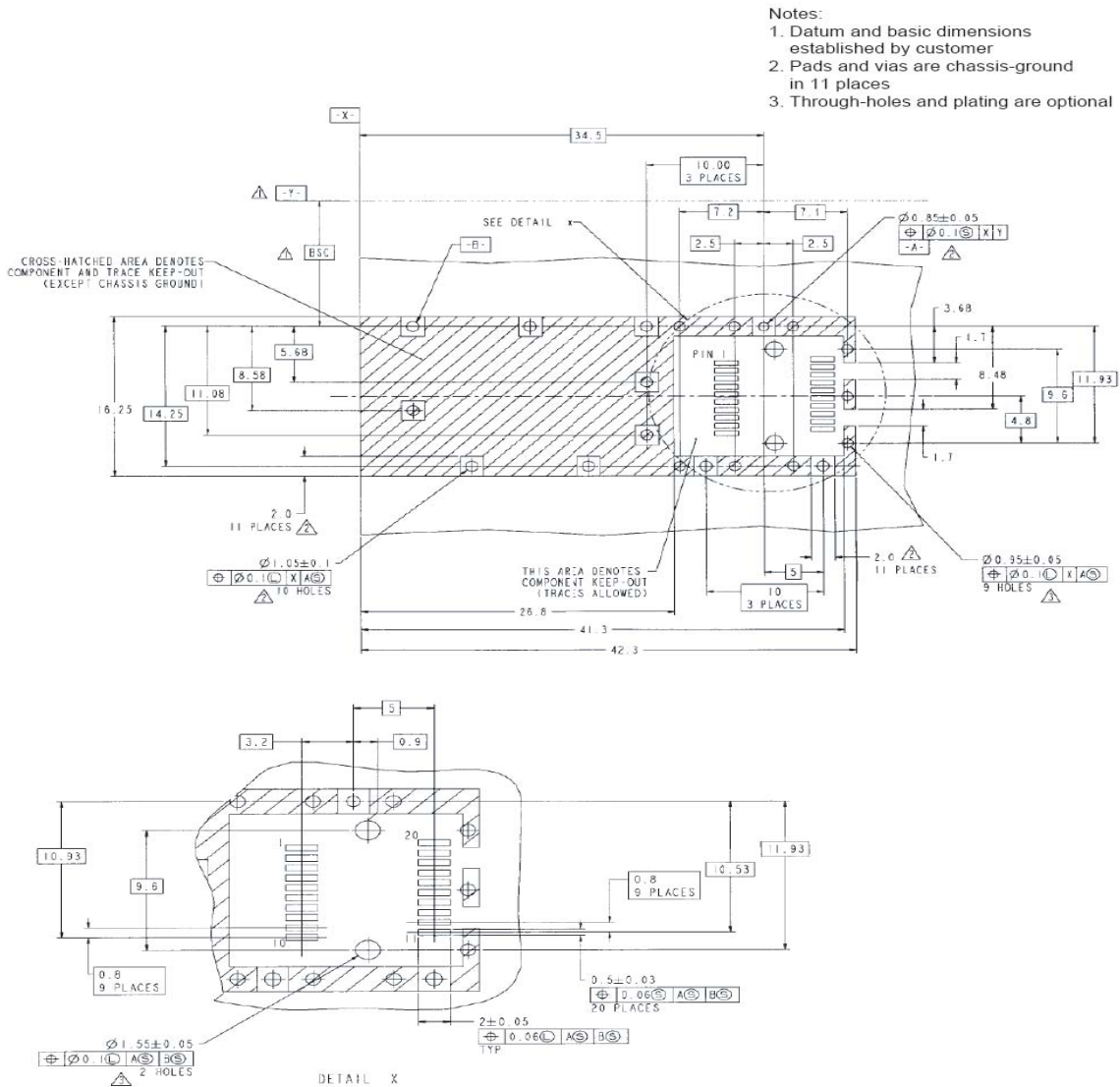


10 Pattern Layout of SFP Printed Circuit Board



(Unit: mm)

11 Recommended PCB Layout



12 Ordering Information

Part Number	Brief Description
EP-2RP-3G-H1	Video Dual Receiver, 3G, DDM, 0~70°C

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